	JP	1	2	3	4	
Base I/O Address		COM4	COM3	COM2	COM1	
Upper row (1)		○	○	○	●	─Serial Port A
Center row (2)		○	○	●	●	─Common
Lower row (3)		○	○	●	○	─Serial Port B

The configuration shown above is the default setting of DMI on port A and COM2 on port B.

COM 1 : 3F8H, COM 2 : 2F8H
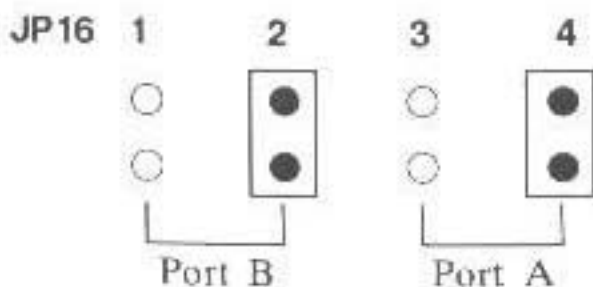COM 3 : 3E8H, COM 4 : 2E8H

4. IRQ SELECTION(JP5~JP15)

Jumpers JP5 to JP15 control the IRQ levels.

The configuration shown below is the default setting of the IRQ level 4 for port A, and IRQ level 3 for port B.

JP	5	6	7	8	9	10	11	12	13	14	15
IRQ	15	14	12	11	10	9	7	6	5	4	3
Port B 1	○	○	○	○	○	○	○	○	○	○	●
2	○	○	○	○	○	○	○	○	○	●	●
Port A 3	○	○	○	○	○	○	○	○	○	●	○

5. CLOCK SOURCE SELECTION(JP16)

KW-509S can provide either 1.8432MHz (Normal) or 3.6864MHz (Double) clock for two serial ports. The jumper JP16 (or SW1) selects which clock frequency feeding into serial ports.

	JP16-3 (SW1-3)	JP16-4 (SW1-4)		JP16-1 (SW1-1)	JP16-2 (SW1-2)
Port A			**Port B**		
Double (3.6864MHz)	short (ON)	open (OFF)	Double (3.6864MHz)	short (ON)	open (OFF)
Normal (1.8432MHz)	open (OFF)	short (ON)	Normal (1.8432MHz)	open (OFF)	short (ON)

Note:( ) if dip type switch used.

6. BOARD layout diagram of KW-509S