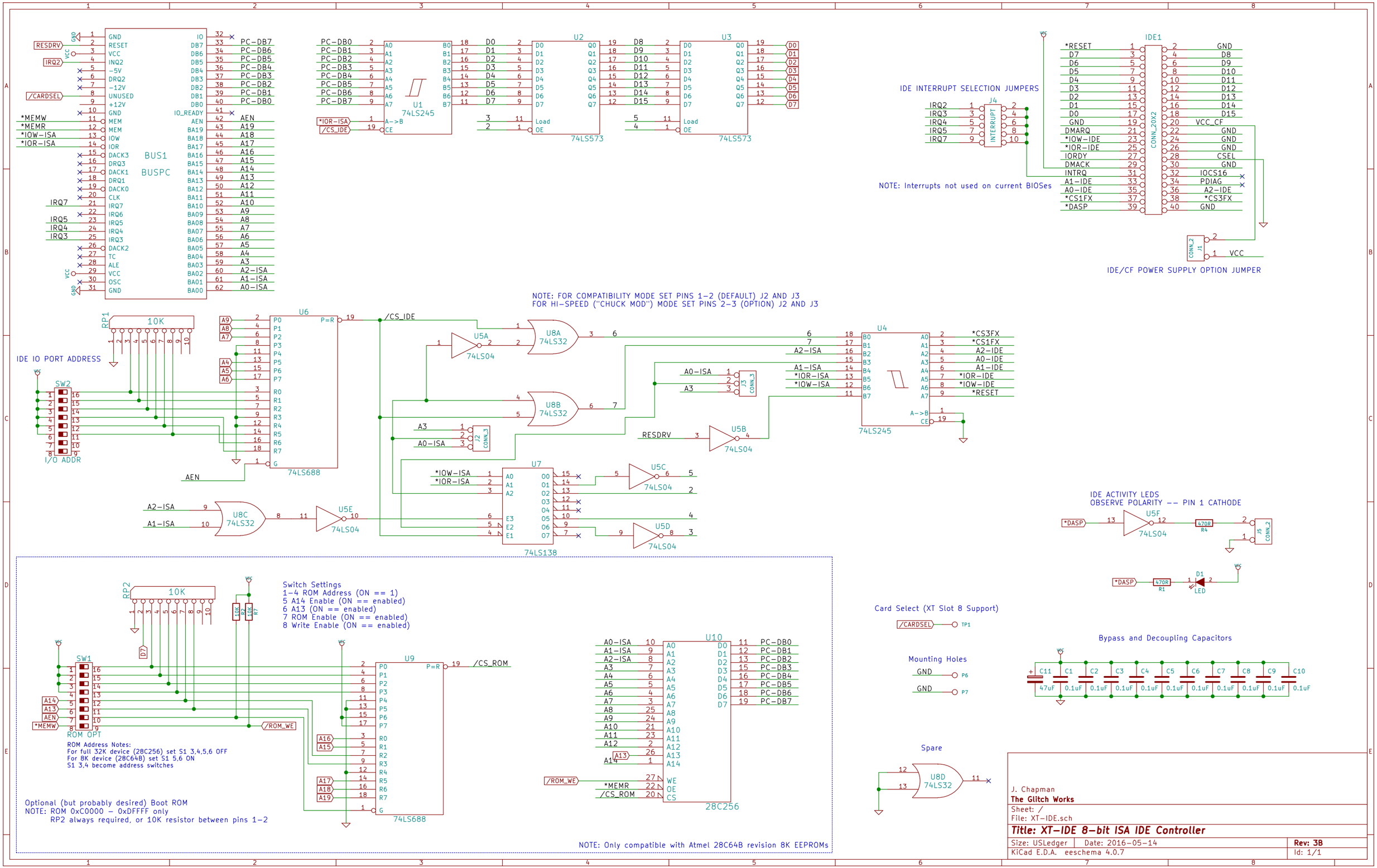
BUS1
BUSPC
U1
74LS245
U2
74LS573
U3
74LS573
IDE1
CONN_20X2
IDE INTERRUPT SELECTION JUMPERS
NOTE: Interrupts not used on current BIOSes
IDE/CF POWER SUPPLY OPTION JUMPER
NOTE: FOR COMPATIBILITY MODE SET PINS 1-2 (DEFAULT) J2 AND J3
FOR HI-SPEED ("CHUCK MOD") MODE SET PINS 2-3 (OPTION) J2 AND J3
IDE IO PORT ADDRESS
I/O ADDR
74LS688
74LS245
74LS138
IDE ACTIVITY LEDS
OBSERVE POLARITY -- PIN 1 CATHODE
Card Select (XT Slot 8 Support)
Mounting Holes
Bypass and Decoupling Capacitors
Spare
Switch Settings
1-4 ROM Address (ON == 1)
5 A14 Enable (ON == enabled)
6 A13 (ON == enabled)
7 ROM Enable (ON == enabled)
8 Write Enable (ON == enabled)
ROM OPT
ROM Address Notes:
For full 32K device (28C256) set S1 3,4,5,6 OFF
For 8K device (28C64B) set S1 5,6 ON
S1 3,4 become address switches
Optional (but probably desired) Boot ROM
NOTE: ROM 0xC0000 - 0xDFFFF only
RP2 always required, or 10K resistor between pins 1-2
28C256
NOTE: Only compatible with Atmel 28C64B revision 8K EEPROMs
J. Chapman
The Glitch Works
Sheet: /
File: XT-IDE.sch
Title: XT-IDE 8-bit ISA IDE Controller
Size: USLedger
Date: 2016-05-14
Rev: 3B
KiCad E.D.A. eeschema 4.0.7
Id: 1/1